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**Cypress Highlights PSoC™ Microcontroller, USB 2.0 Solutions,  
Timing Technology, and Memory Solutions At Embedded Systems Conference**

*Embedded Systems Conference  
Booth #634  
Moscone Center, South Hall  
San Francisco, California  
April 9 – 12, 2001*

SAN JOSE, Calif., April 6, 2001 – Cypress Semiconductor (NYSE: CY) today announced that it will exhibit at the Embedded Systems Conference, to be held at the Moscone Convention Center, South Hall, San Francisco, California, from April 9 through April 12, 2001. Cypress MicroSystems, a Cypress Semiconductor spin-off, will highlight its Programmable System-on-a-Chip™ (PSoC™) microcontroller and development tools, along with Cypress's timing technology and next-generation memory solutions. Cypress will exhibit these solutions at Booth #634.

Cypress MicroSystems, a subsidiary of Cypress, will show its PSoC devices, which integrate a fast microcontroller, SONOS-based (Silicon Oxide Nitride Oxide Silicon) Flash memory and SRAM, and programmable arrays of analog and digital system functions – known as PSoC blocks – in low-cost, small-footprint packages. To save designers time, Cypress MicroSystems offers User Modules – pre-designed peripherals comprised of PSoC blocks. By selecting a PSoC with the needed resource combination of memory, PSoC blocks and pins, designers of communications, computing, consumer, and industrial applications have a device that reduces costs by eliminating external chips and simplifying system design.

Cypress's Interface Products Division will demonstrate the EZ-USB® FX2, the world's first USB

-MORE-

2.0 integrated peripheral controller, including an 8051 processor, a serial interface engine (SIE), a USB transceiver, on-chip RAM and FIFO and a general programmable interface. FX2 is a fully integrated solution, requiring less board space and providing faster time to market. At 480 MHz, it is 40x faster than USB 1.1, making it the perfect solution for high-bandwidth connections.

Cypress's Timing Technology Division will demonstrate its field-programmable clock generator with EPROM programmability. With field programmability, designers can now specify custom frequencies and program devices immediately, rather than waiting weeks for mask layers to be customized, or months for custom crystals or metal-can oscillators.

Cypress's Memory Products Division will exhibit a variety of devices for embedded systems technology including More Battery Life™ (MoBL™), Quad Data Rate (QDR™), and Sync Burst-Pipelined devices.

- Cypress's MoBL SRAMs have the industry's lowest power consumption, using up to 90% less power than current standard, low-power SRAMs.
- The Cypress QDR memories increase bandwidth by supporting separate data inputs and outputs for read and write operations. Independent ports transferring data on both edges of the clock result in a quad-speed transfer of data. The result is maximum bandwidth, with the capability to deliver data as fast as 333 MHz, and the elimination of the need to "turn around" the data bus, which is required when using common I/O devices.
- The Synchronous Burst SRAM family employs high-speed, low-power CMOS designs using advanced triple-layer polysilicon, double-layer metal technology. They optimize performance in networking applications, offering a "byte write" feature that enables data to be changed in one operation, versus three operations in the current "read/modify/write" method.

### **About Cypress**

Cypress Semiconductor is "Driving the Communications Revolution"™ by providing high-performance integrated circuit solutions to fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial control. With a focus on emerging communications applications, Cypress's product portfolios include high-speed data communications ICs; networking-optimized and micropower static RAMs; high-bandwidth multi-port and FIFO memories; high-density programmable logic devices; timing technology solutions; and controllers for Universal Serial Bus (USB).

More than two-thirds of Cypress's sales come from fast-growing communications markets and

-MORE-

dynamic companies such as Alcatel, Cisco, Ericsson, Lucent, Motorola, Nortel Networks, and 3Com. Cypress's ability to mix and match its broad portfolio of intellectual property enables targeted, integrated solutions for high-speed systems that feed bandwidth-hungry Internet applications. Cypress aims to become the preferred silicon supplier for Internet switching systems and for every Internet data stream to pass through at least one Cypress IC.

Cypress employs more than 4,500 people worldwide with international headquarters in San Jose, California. Its shares are listed on the New York Stock Exchange under the symbol CY. More information about Cypress is accessible electronically on the company's worldwide Web site at <http://www.cypress.com>.

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“Safe Harbor” Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are “forward-looking statements” involving risks and uncertainties, including by not limited to: the effect of global economic conditions, shifts in supply and demand, market acceptance, the impact of competitive products and pricing, product development, commercialization and technological difficulties, and capacity and supply constraints. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

Programmable System-on-a-Chip, PSoC, More Battery Life, MOBL, QDR, and “Driving the Communications Revolution” are trademarks of Cypress Semiconductor. EZ-USB is a registered trademark of Cypress Semiconductor.

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